A picture containing text, electronics, computer

Description automatically generated

Run is triggered to start the Basic I/O Test 2 program

Switches are first set to 10’b 00 0000 0000

Switches are set to 10’b 00 0000 1011

(16’h000B) to jump to the start address x000B of the Self-Modifying Code Test program

The program goes into Pause state and wait for Continue to be triggered

LED value is incremented by 1 each time after Continue is triggered

Reset the entire SLC-3 processor

All registers are cleared

FSM is set to Halted state

Trigger Continue